Appl. No. 09/873,242   ASA-1008
Amendment filed December 6, 2004
Reply to Office Action of Final Rejection
  of July 6, 2004 and Advisory Action of November 15, 2004

REMARKS

Claims 1-4 and 8 have been amended. No claims have been deleted or added. Accordingly, claims 1-15 remain pending in this application.

Title

In the Advisory Action mailed November 15, 2004, the Examiner did not indicate in Box 7 whether or not the proposed amendments would be entered. Therefore the amendments to the title from the Amendment filed November 2, 2004, have been repeated herein to correct minor informalities.

35 U.S.C. §112

Furthermore, the Remarks contained in the Amendment' filed November 2, 2004, are hereby incorporated by reference. Specifically, the Remarks point out that the "transfer number designation register" is a part of the "data transfer controller". The Examiner's attention is directed to Fig. 2 and its corresponding description, e.g., at pages 20-22 of the specification. Additionally, claim 8 has been amended in this Amendment to further clarify the claim language. Accordingly,

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the rejection of claim 8 under 35 U.S.C. §112 should be

withdrawn.

35 U.S.C. §§102 and 103

Claims 1-8 and 10-15 stand rejected under 35 U.S.C.

§102(b) as being anticipated by Mitsuhira et al. (U.S. Patent

No. 5,325,489). Claim 9 stands rejected under 35 U.S.C.

§103(a) as being unpatentable over Mitsuhira et al. These

rejections are traversed as follows.

The present invention includes a data transfer controller

in which a control unit continues to transfer data to a memory

of a predetermined storage capacity, and cyclically repeats

data transfer of a predetermined data amount. The memory is

divided into a plurality of storage areas, and an interrupt is

issued each time data transfer is completed to each divided

storage area. Claim 1, as amended, recites two functions, one

of which is an initializing function, and the other of which

is the issuance of an interrupt each time data transfer of a

predetermined amount is completed within a loop range until

the transfer destination address has been initialized under

the first function. Thus, under the invention, initialization

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is performed automatically, without the requirement that an external request be received, such as from the CPU.

The rejections in the prior Office Actions have only set forth part of the invention, and have ignored the automatic re-initialization feature of the invention, which is autonomously performed without receiving an additional external request or instruction from the CPU. However, this distinction may not have been apparent to the Examiner because claim 1 did not recite a specified area that is repeatedly used and overwritten. Accordingly, to clarify this feature, Applicants have amended independent claims 1-4 so that a specified transfer destination address is initialized for automatically transferring and overwriting data to the initialized specified area. Neither Mitsuhira, nor the other art of record, taken either singly, or in combination, show or suggest this feature. Thus, claims 1-4 are considered to be allowable, and claims 5-15 are allowable at least because they depend from an allowable base claim.

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Conclusion

In view of the foregoing, Applicant respectfully requests

that a timely Notice of Allowance be issued in this case.

Respectfully submitted,

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Amendment to the Title

Please amend the title as follows:

A SYSTEM FOR TRANSFERRING DATA ~~OF~~BY REUSING A PLURALITY OF

DATA TRANSFERRING AREA<u>S</u> CYCLICALLY.